



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Sumio OGAWA et al.
Title: METHOD OF MANUFACTURING SEMICONDUCTOR
DEVICES
Appl. No.: 10/044,964
Filing Date: 01/15/2002
Examiner: Shella S. Rao
Art Unit: 2125

**REQUEST FOR ACKNOWLEDGEMENT OF INFORMATION DISCLOSURE
STATEMENT**

Commissioner for Patents
PO Box 1450
Alexandria, Virginia 22313-1450

Sir:

In reviewing the above-captioned application file upon allowance, the undersigned has noted that PTO Form-SB08, submitted on June 10, 2003, has not been acknowledged by the Patent and Trademark Office.

The Patent and Trademark Office is, therefore, requested to return the initialed forms to the undersigned as soon as possible.

Respectfully submitted,

Date September 7, 2005

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